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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideto Ohnuma et al. Art Unit : Unknown
Serial No. : Unassigned Examiner : Unknown
Filed : August 10, 2001
Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231


INFORMATION DISCLOSURE STATEMENT

Applicants submit the reference listed on the attached form PTO-1449, a copy of which is enclosed.

This statement is being filed with the application. Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: August 10, 2001



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-068001	Application No.
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant Hideto Ohnuma et al.	
		Filing Date August 10, 2001	Group Art Unit

1-5978 U.S. PTO
09/25/01
08/10/01

U.S. Patent Documents

Examiner Initial	Desig. ID	Patent Number	Issue Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AL							
	AM							
	AN							
	AO							
	AP							

Other Documents (include Author, Title, Date, and Place of Publication)

Examiner Initial	Desig. ID	Document
	AQ	C.W. Kim et al.; "42.1: A Novel Four-Mask-Count Process Architecture for TFT-LCDs"; <u>SID 00 Digest</u> ; pp. 1006-1009; 2000
	AR	
	AS	
	AT	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	